



(19)

(11) Publication number: **20**

Generated Document.

PATENT ABSTRACTS OF JAPAN(21) Application number: **10241915**(51) Intl. Cl.: **G02F 1/313 G02B 6/12 F**(22) Application date: **27.08.98**

(30) Priority:		(71) Applicant: NEC CORP
(43) Date of application publication:	14.03.00	(72) Inventor: HAMAMOTO KIICHI
(84) Designated contracting states:		(74) Representative:

**(54) SEMICONDUCTOR
OPTICAL GATE
AMPLIFIER SWITCH**

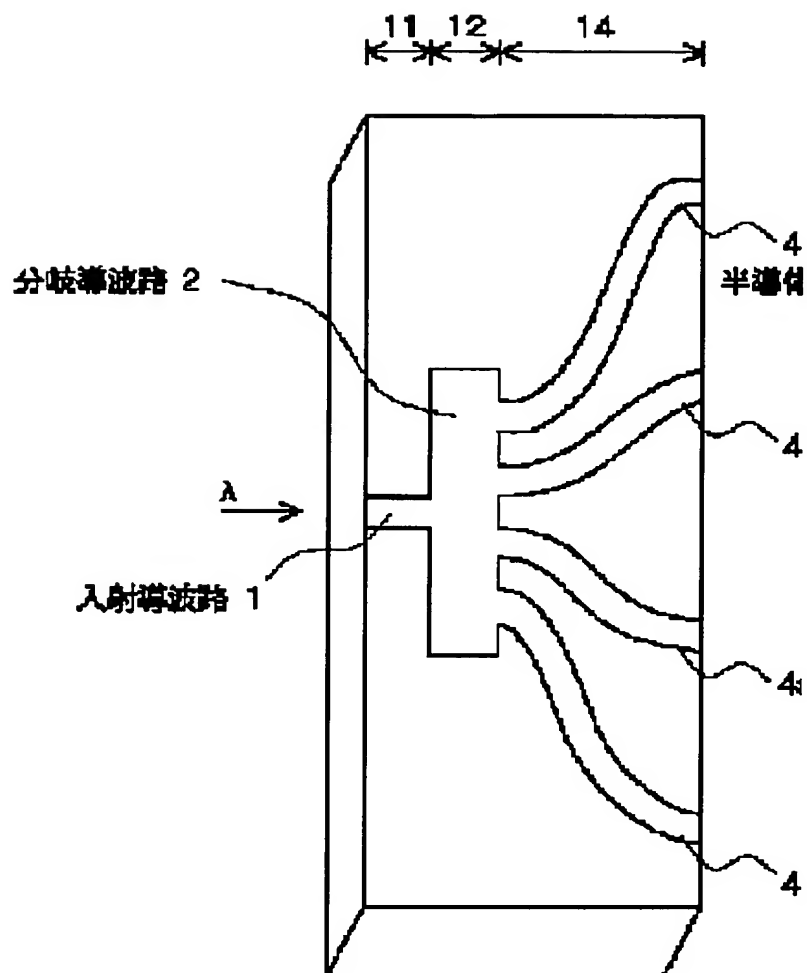
(57) Abstract:

PROBLEM TO BE SOLVED: To obtain an optical switch which is small in size, is relatively simple in structure and may be produced at a good yield by including curvilinear waveguides into N pieces of semiconductor amplifiers.

SOLUTION: The semiconductor optical amplifier gate type switch of a 1×4 type comprise an incident waveguide 1, branch waveguides 2 of an MMI (multimode interference) structure of the 1×4 type and semiconductor amplifier gates 41 to 44 including the curvilinear waveguides of 300 μm in curvature. The switch is divided to an incident waveguide region 11 corresponding to the incident waveguide 1 and a branch waveguide region 12 corresponding to the branch waveguides 2 and semiconductor amplifier gate regions 14 corresponding to the semiconductor optical amplifier gates 41 to 44. All these regions are formed as active

regions. The incident waveguide region 11 and the branch waveguide regions 12 are driven as optical amplifier regions by the same electrodes. The respective semiconductor optical amplifier gates 41 to 44 are driven by using respectively different electrodes, by which gate operation is carried out.

COPYRIGHT: (C)2000,JPO



BEST AVAILABLE COPY